

FIGURE 1 (PRIOR ART)

FIG. 1 is a block diagram of a system 100 in accordance with one embodiment of the present invention. The system 100 includes a processor 102, a memory 104, a network interface 106, and a user interface 108. The processor 102 is connected to the memory 104, the network interface 106, and the user interface 108. The network interface 106 is connected to a network 110. The user interface 108 is connected to a user 112. The system 100 is configured to receive data from the network 110 and provide data to the user 112.

AFTER

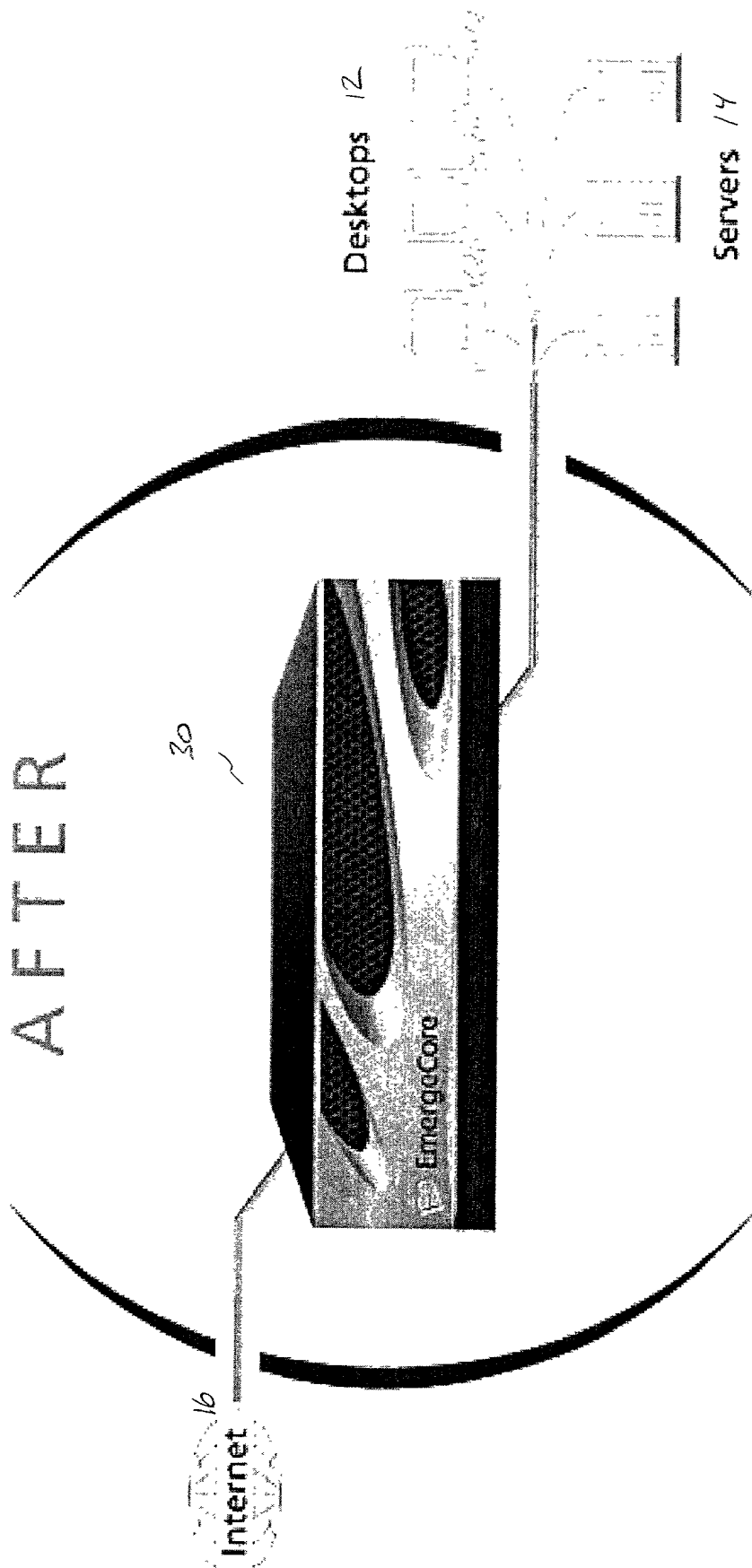


FIGURE 2

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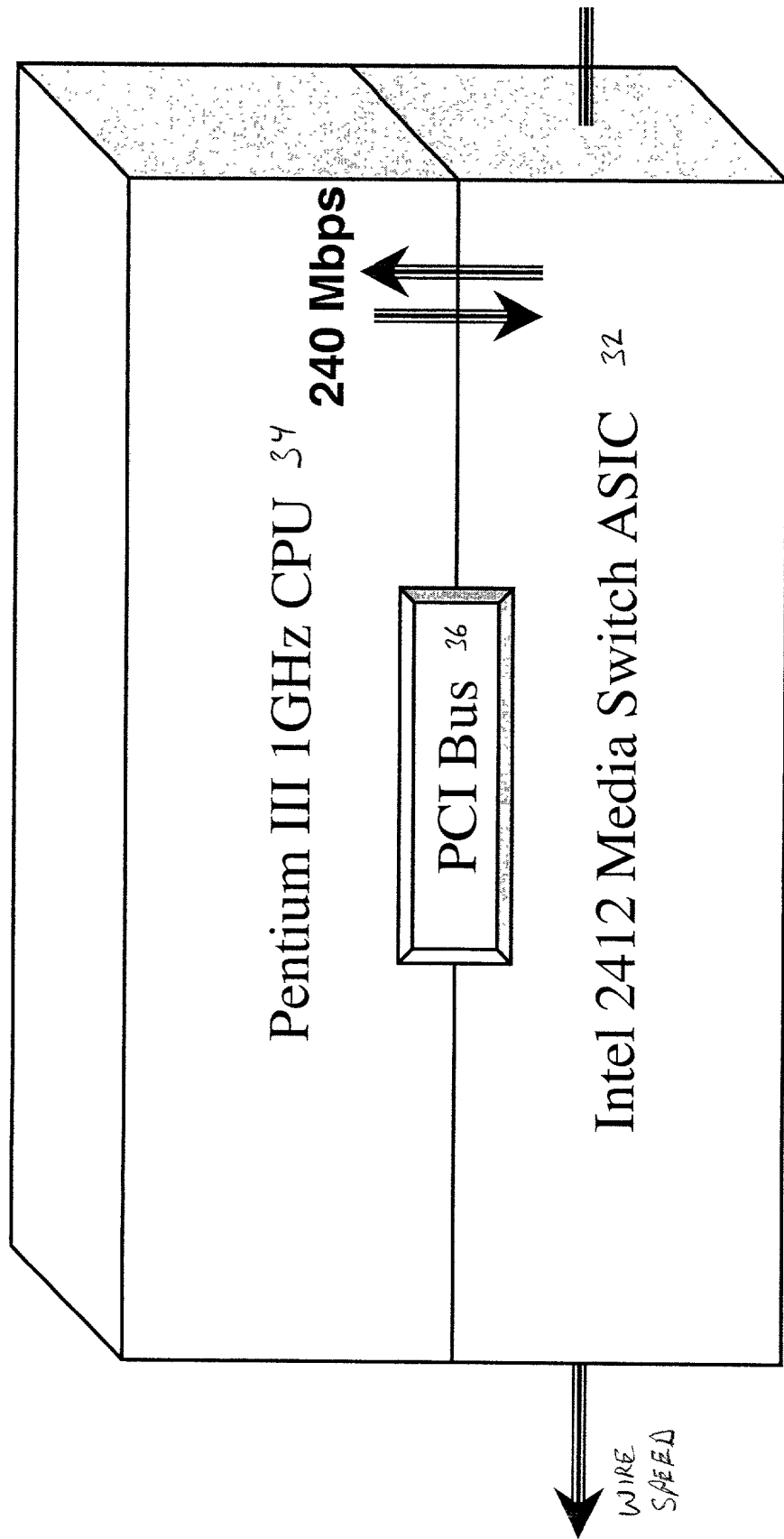
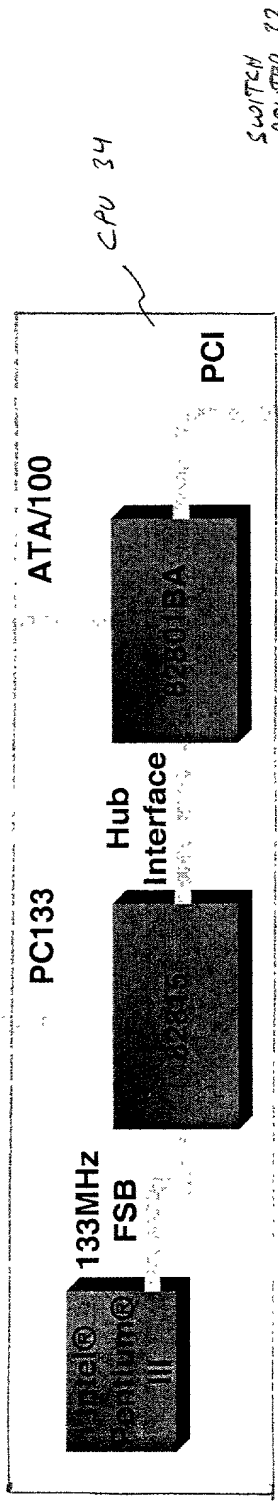
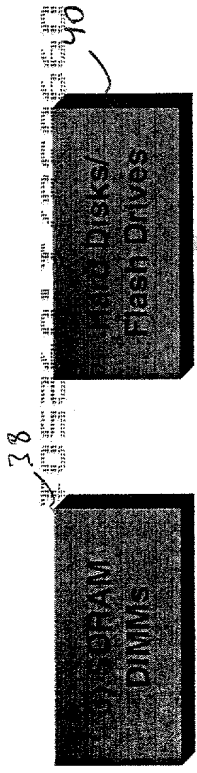


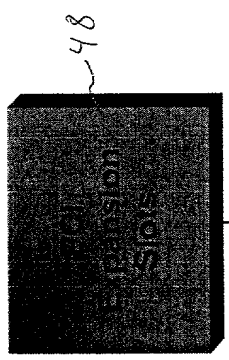
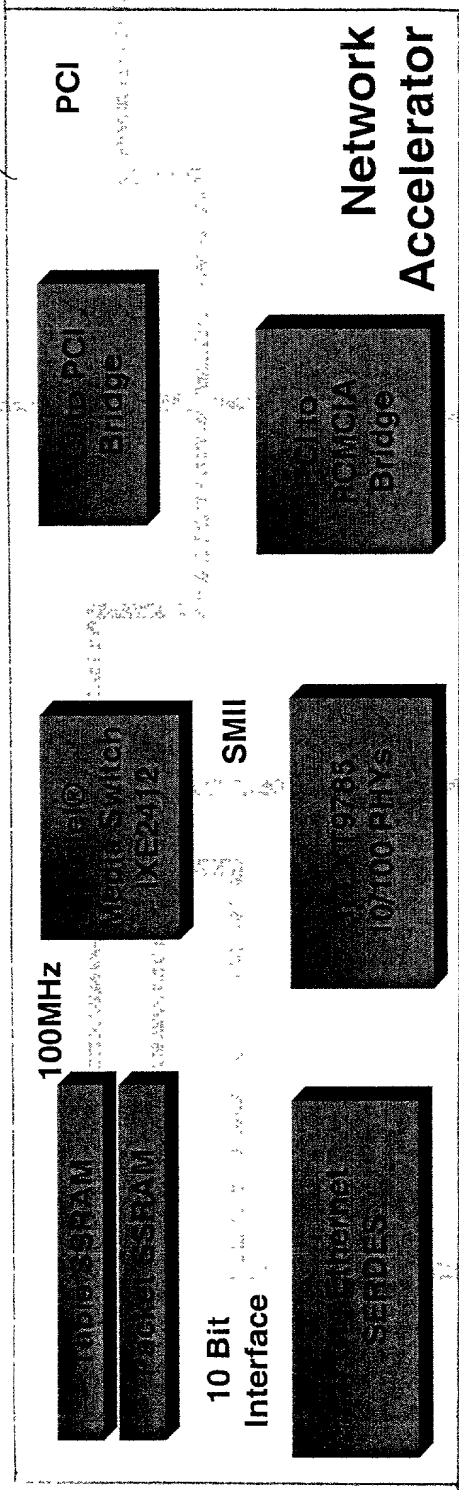
FIGURE 3

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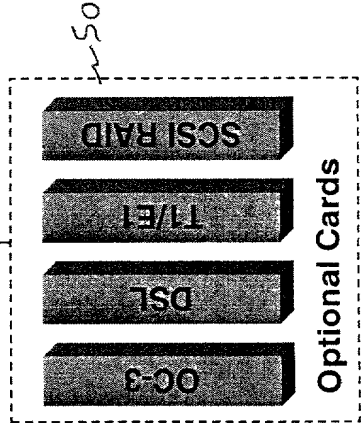


SWITCH ROUTER 32

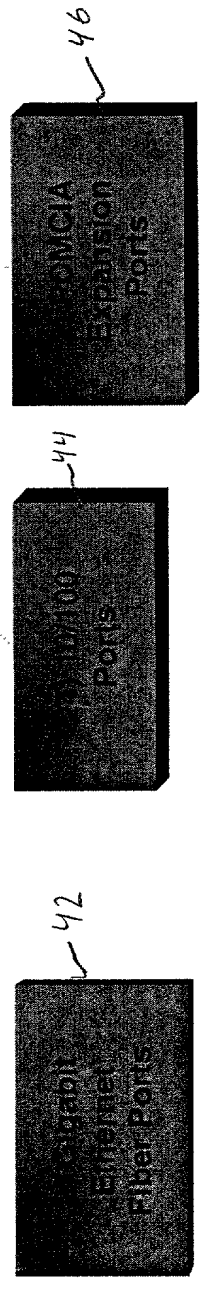
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FIGURE 4

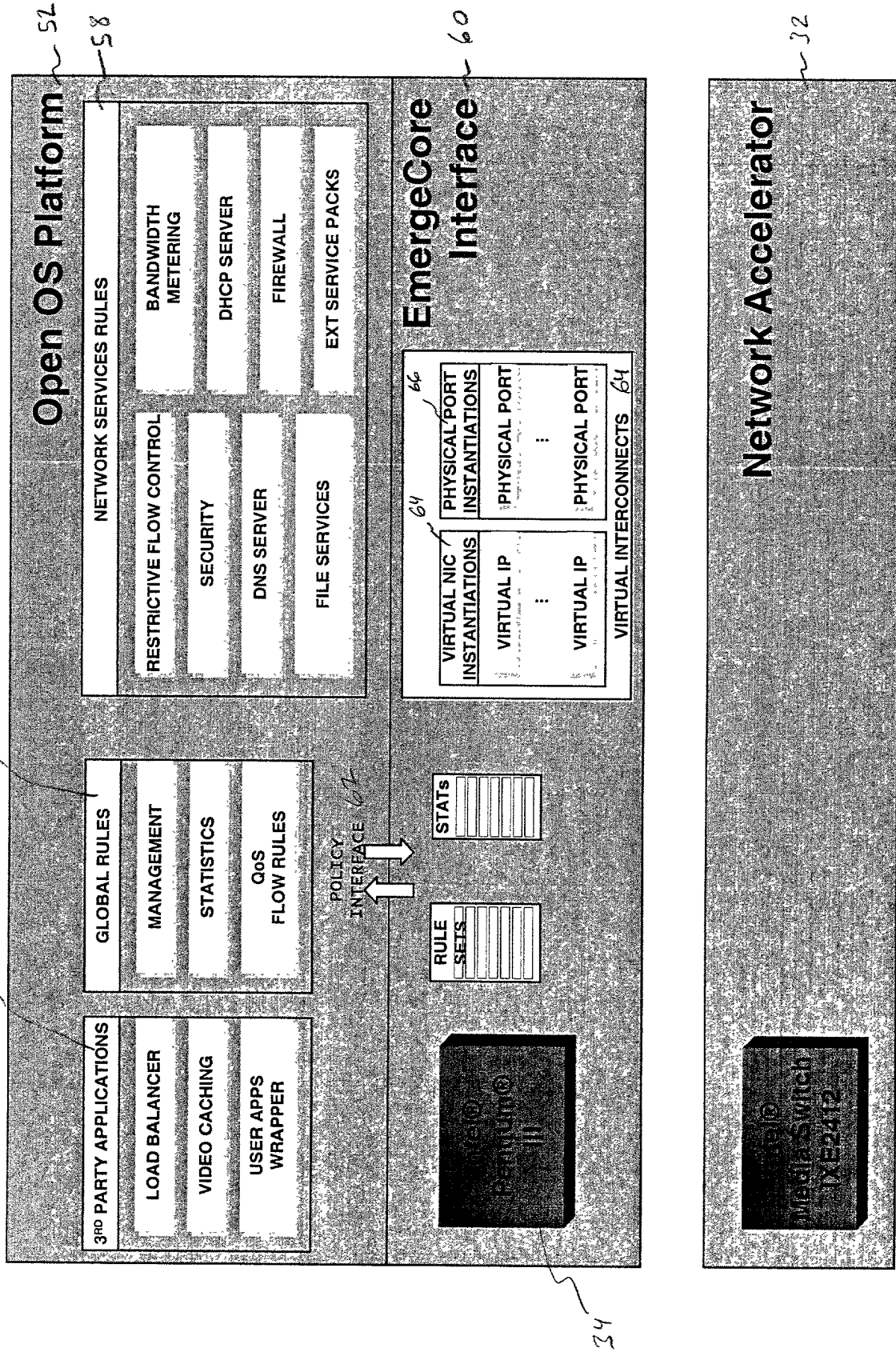


FIGURE 5

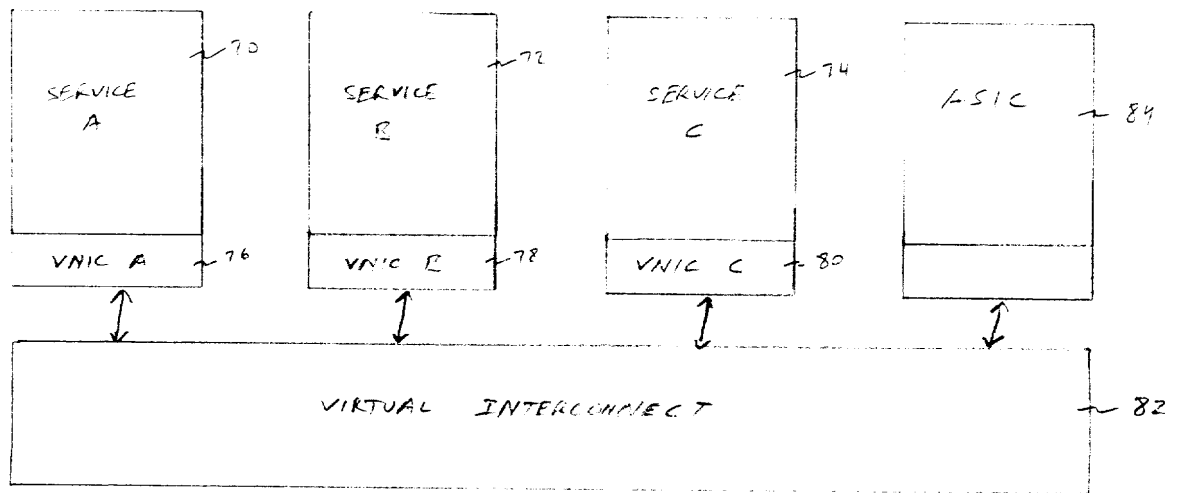


FIGURE 6